

Class	Subclass

**CPA**

01-1082

UTILITY SERIAL NUMBER	PATENT DATE	PATENT NUMBER
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SERIAL NUMBER 08/897,953	FILING DATE 07/24/97	CLASS 431	SUBCLASS 1	GROUP ART UNIT	EXAMINER
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\*\*\*CONTINUING DATA\*\*\*\*\*  
VERIFIED THIS APPLN IS A CON OF 08/393,677 07/24/97

\*\*\*FOREIGN/PCT APPLICATIONS\*\*\*\*\*  
VERIFIED JAPAN 6-088762 04/26/99

**CPA**

Foreign priority claimed 35 USC 119 conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	AS FILED	STATE OR COUNTRY JPX	SHEETS DRWGS. 6	TOTAL CLAIMS 10	INDER CLAIMS	FILING FEE RECEIVED	ATTORNEY'S DOCKET NO. 7501075
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TITLE: METHOD AND APPARATUS FOR FABRICATING SEMICONDUCTOR DEVICE  
U.S. DEPT. OF COMM / PAT. & TM -- PTO-436L (Rev.12-94)

PARTS OF APPLICATION FILED SEPARATELY		Applications Examiner	
NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg	Figs. Drwg. Print Fig.
Label Area		ISSUE BATCH NUMBER	
Primary Examiner		PREPARED FOR ISSUE	

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